

ACADEMIC YEAR 2026-27 : ODD SEMESTER

Scheme: PRR20

M.Tech. Embedded System & VLSI- I SEMESTER

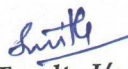
Class Teacher: Sri G. Raju

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Day	09:30-10:30	10:30-11:30	11:30-12:30	12:30-01:30	L U N C H B R E A K	02:30-03:30	03:30-04:30
MON	EPRW SH-3-5	RMIPR SH-3-5	ESD	MBES		→ LIBRARY ←	
TUE	ESD	SVDV	DICD	MBES		RMIPR SH-3-5	EPRW SH-3-5
WED	SVDV	ESD	SVDV(T)	DICD(T)		→ LIBRARY ←	
THU	DICD	ESD(T)	→ MBES LAB (B-I-201) ← (KSJ)			→ LIBRARY ←	
FRI	MBES	DICD	SVDV	MBES (T)		→ DICD LAB (B-I-219) ← (GR)	
SAT	-	-	-	-			

Course Title	Course Code & Course Name		Course Faculty
DICD	P20EV101: Digital IC Design	-	Sri G. Raju (GR)
MBES	P20EV102: Microcontroller based Embedded Systems	-	Dr. K. Shailaja(KSJ)
SVDV(PE-I)	P20EV103B: System Verilog for Design and Verification	-	Faculty X
ESD (PE-II)	P20EV104A: Embedded System Design	-	Faculty Y
RMIPR	P20MC107: Research Methodology and IPR	-	Dr. G. Srinivas Rao (GSR)
ERPW	P20AC108A: English for Research Paper Writing	-	Dr. B. Subhash (BS)
DICD LAB	P20EV105: Digital IC Design Laboratory	-	Sri G. Raju (GR)
MBES LAB	P20EV106: Microcontroller based Embedded Systems Laboratory	-	Dr. K. Shailaja(KSJ)

Note: It is to inform to the 1EV students that they shall approach Dr. B. Jeevan @M. Tech.(EV) Coordinator for any clarifications / requirements.


Faculty I/c,
Dept. Timetables


Faculty I/c,
Overall Timetables


Head of the Department


Dean, AA


Principal